

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Vernon et al. Art Unit: 1756  
Serial No.: 10/808,063 Examiner: Stephen D. Rosasco  
Filed: March 26, 2004 Assignee: Intel Corporation  
Title: CONTINUOUS SLOPED PHASE EDGE ARCHITECTURE  
FABRICATION TECHNIQUE USING ELECTRON OR OPTICAL BEAM  
BLUR FOR SINGLE PHASE SHIFT MASK RET

**MAIL STOP AF**

Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

AMENDMENT

In response to the Office action mailed March 19, 2007,  
please reconsider this application in light of the following:

Amendments to the claims set forth in the **Listing of Claims**  
beginning on page 2; and

**Remarks** beginning on page 8.